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PATENT Customer No. 22,852 Attorney Docket No. 4173.0441

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:	)
HISASHI KANEKO ET AL.	)
Application No.: 10/757,415	) Group Art Unit: 2123
Filed: January 15, 2004	) Examiner: Unknown
For: SIMULATION CIRCUIT PATTERN EVALUATION METHOD, MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED CIRCUIT, TEST SUBSTRATE, AND TEST SUBSTRATE GROUP	) ) ) )

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

## **INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97(b)**

Pursuant to 37 C.F.R. §§ 1.56 and 1.97(b), applicants bring to the attention of the Examiner the document listed on the attached PTO 1449. This Information Disclosure Statement is being filed before the mailing date of a first Office Action on the merits for the above-referenced application.

A copy of the listed document is attached.

Applicants respectfully request that the Examiner consider the listed document and indicate that it was considered by making an appropriate notation on the attached form.

This submission does not represent that a search has been made or that no better art exists and does not constitute an admission that the listed document is material or constitutes "prior art." If the Examiner applies the document as prior art against any claim in the application and applicants determine that the cited document does not constitute "prior art" under United States law, applicants reserve the right to present to the office the relevant facts and law regarding the appropriate status of the document.

Applicants further reserve the right to take appropriate action to establish the patentability of the disclosed invention over the listed document, should the document be applied against the claims of the present application.

If there is any fee due in connection with the filing of this Statement, please charge the fee to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW, GARRETT & DUNNER, L.L.P.

Dated:

D.

Richard V Burquii

Reg. No. 31,744



Atty. Docket No.	TERROPHEN .	Appln. No.	10/757,415	
Applicant	Kaneko et al.			
Filing Date	January 15, 2004	Group:	2123	

U.S. PATENT DOCUMENTS						
Examiner Initial*	Document Number	Issue Date	Name	Class	Sub Class	Filing Date If Appropriate
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FOREIGN PATENT DOCUMENTS						
	Document Number	Publication Date	Country	Class	Sub Class	Translation Yes or No

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)			
	U.S. Patent Application No. 10/784,939, filed February 25, 2004, to Kadota		

Examiner		Date Considered		
*Examiner:	er: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.			
Form PTO 144	9 Patent and	Trademark Office - U.S. Department of Commerce		